



This scanning electron microscope (SEM) image shows a microchip die with a complex circuit layout overlaid in green. The layout includes various functional blocks, interconnects, and peripheral structures. The die is rectangular and surrounded by a dark border. The overlaid circuitry is composed of numerous rectangular blocks of varying sizes, interconnected by a network of lines. The text in the image provides metadata about the chip's origin and specifications.

& Vendor GLOBALFOUNDRIES

& Product GF018hw5v_mcu_sc7

& Version 2015q2v1

& Metric 1.00

2 μm



A horizontal scale bar is located at the bottom left of the image, corresponding to the 2 μm label.